

WEST Search History

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DATE: Tuesday, January 18, 2005

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|---------------------------------------|-----------------|--|------------------|
| <i>DB=JPAB; PLUR=YES; OP=ADJ</i> | | | |
| <input type="checkbox"/> | L14 | L13 AND POWER SUPPLY | 4 |
| <input type="checkbox"/> | L13 | L12 AND COUPLING ADJ3 (BIT OR CAPACIT\$) | 85 |
| <input type="checkbox"/> | L12 | MEMORY AND SENSS | 28332 |
| <i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i> | | | |
| <input type="checkbox"/> | L11 | L9 AND CAPACITOR ADJ12 BIT\$ | 118 |
| <input type="checkbox"/> | L10 | L8 AND DRAM | 166 |
| <input type="checkbox"/> | L9 | L8 AND (DRAM OR DYNAMIC ADJ4 RANDOM) | 175 |
| <input type="checkbox"/> | L8 | L7 AND BIT ADJ5 COUPL\$ | 254 |
| <input type="checkbox"/> | L7 | L6 AND CAPACITANCE | 725 |
| <input type="checkbox"/> | L6 | L5 AND COUPLING ADJ3 CAPACIT\$ | 869 |
| <input type="checkbox"/> | L5 | L4 AND POWER SUPPLY | 9687 |
| <input type="checkbox"/> | L4 | L3 AND BIT\$ | 22839 |
| <input type="checkbox"/> | L3 | L2 AND SENSS ADJ2 AMPLIF\$ | 25682 |
| <input type="checkbox"/> | L2 | L1 AND CELL\$ | 224896 |
| <input type="checkbox"/> | L1 | MEMORY | 692133 |

END OF SEARCH HISTORY

VIET Q. NGUYEN
PRIMARY EXAMINER